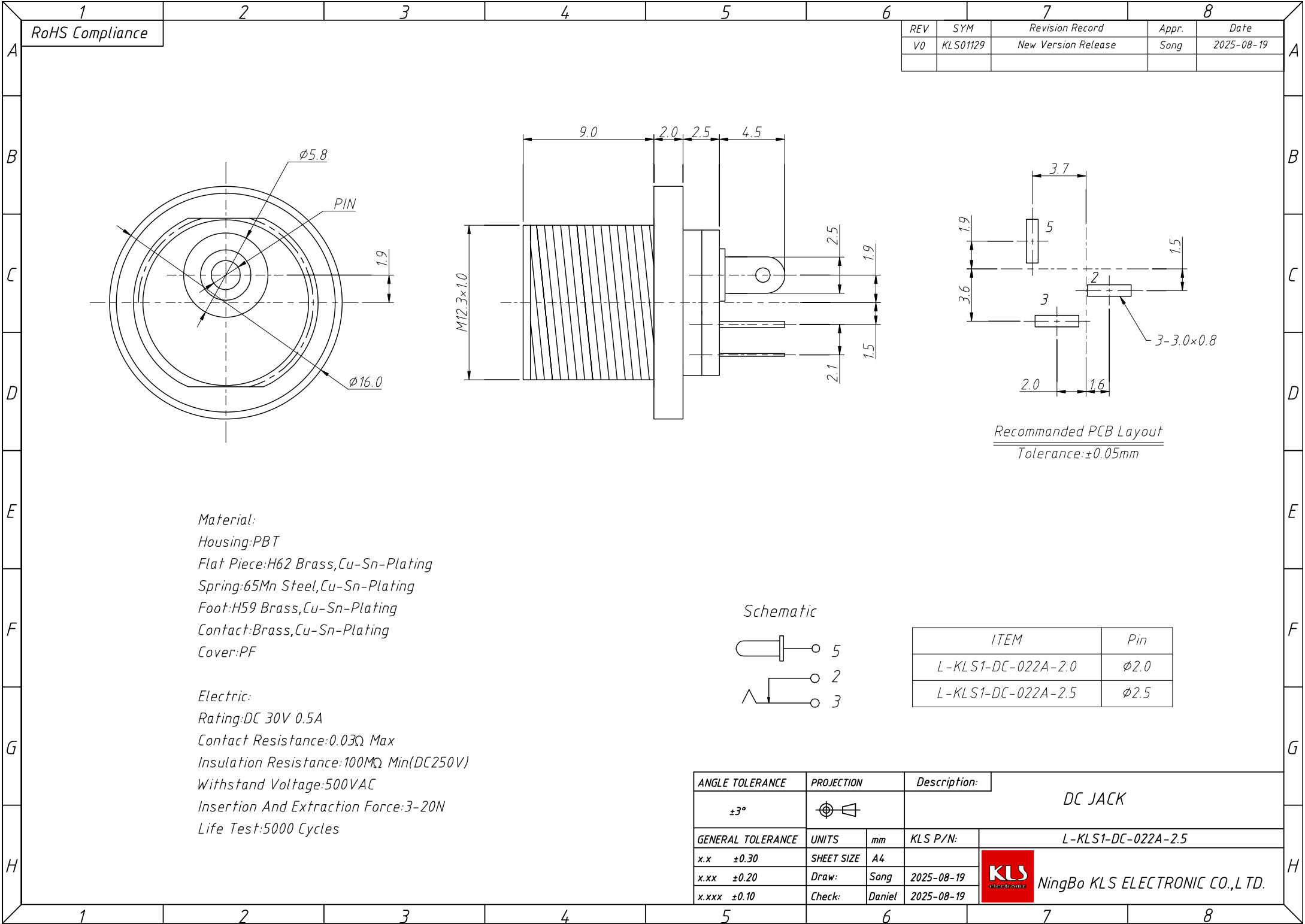
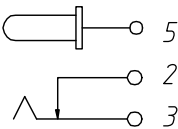




6		7		8	
REV	SYM	Revision Record		Appr.	Date
V0	KLS01129	New Version Release		Song	2025-08-19

Recommanded PCB Layout
Tolerance:±0.05mm

Schematic



ITEM	Pin
L-KLS1-DC-022A-2.0	φ2.0
L-KLS1-DC-022A-2.5	φ2.5

ANGLE TOLERANCE		PROJECTION		Description:	
±3°				DC JACK	
GENERAL TOLERANCE		UNITS	mm	KLS P/N:	
x.x	±0.30	SHEET SIZE	A4	L-KLS1-DC-022A-2.5	
x.xx	±0.20	Draw:	Song	2025-08-19	NingBo KLS ELECTRONIC CO.,L TD.
x.xxx	±0.10	Check:	Daniel	2025-08-19	