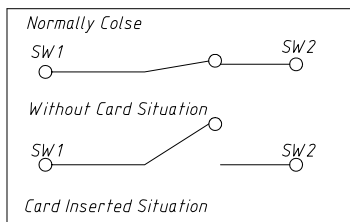
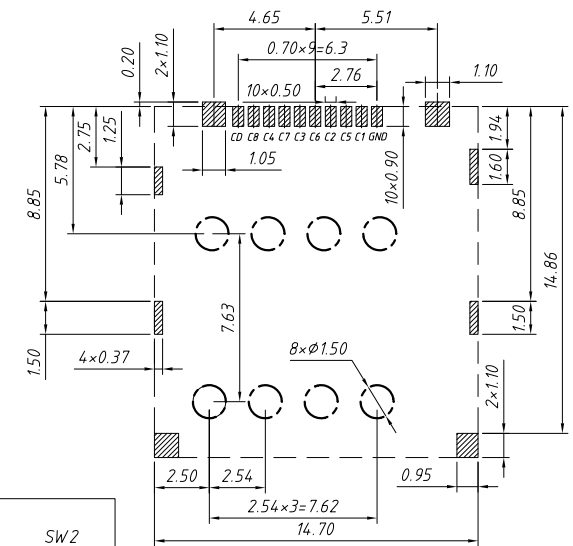


Pin No.	NAME
C1	VCC
C2	RST
C3	CLK
C4	Reserved
C5	GND
C6	VPP
C7	I/O
C8	Reserved
WS1	C/D
SW2	GND

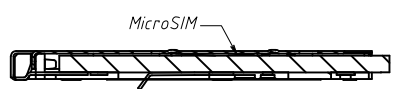
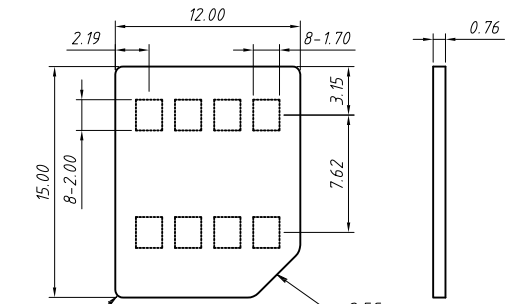
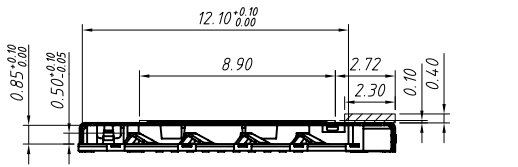
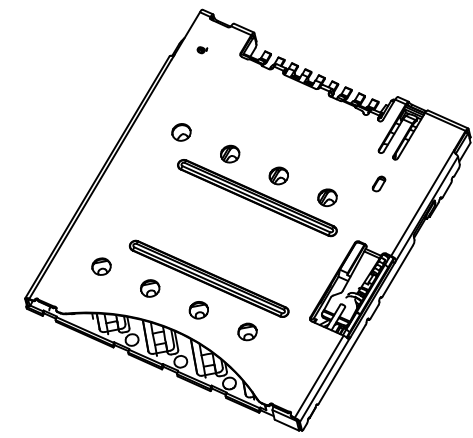


□: NO WIRING AREA
 ▨: BOARD PAD
 ▲ NOTE 1: ○: PCB Through hole

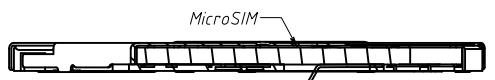
Figure 5.2 Recommended PCB Layout
 GENERAL TOLERANCE ±0.05

Material:
 Insulator: High Temperature Thermoplastic, UL 94 V-0, Black.
 Contact: Copper Alloy, Gold or Nickel.
 Shell: Stainless, Gold or Nickel.
 Solder Area: Matte Tin 80u" Plated Gold Flash.

Electrical:
 Current Rating: 1A
 Rating Voltage: 30V
 Contact Resistance: 100mΩ Max.
 Insulation Resistance: 1000MΩ Min./500V DC
 Dielectric Withstanding Voltage: 500V AC
 Ambient Temperature Range: -20°C ~ +85°C
 Storage Temperature Range: -40°C ~ +85°C
 Ambient Humidity Range: 95% R.H. Max.
 Peak Temperature: 260°C ±5°C
 Mating Cycles: 5000 Insertions



The ends of the terminals reveal the outer surface of the product.
 SEC A-A



The ends of the terminals reveal the outer surface of the product.
 SEC B-B

GENERAL TOLERANCE		ANGLE TOLERANCE		PROJECTION	Description:
X	±0.15	X	±5°	UNITS	mm
.X	±0.03	.X	±3°	SHEET SIZE	A4
.XX	±0.05	.XX	±2°	KLS P/N:	L-KLS1-SIM-095-H1.28-R
Draw by:	Jenny	Date:	2018-03-10	NingBo KLS ELECTRONIC CO.,LTD.	
Check by:		Date:	2018-03-10		
SCALE		SHEET	1 OF 1		