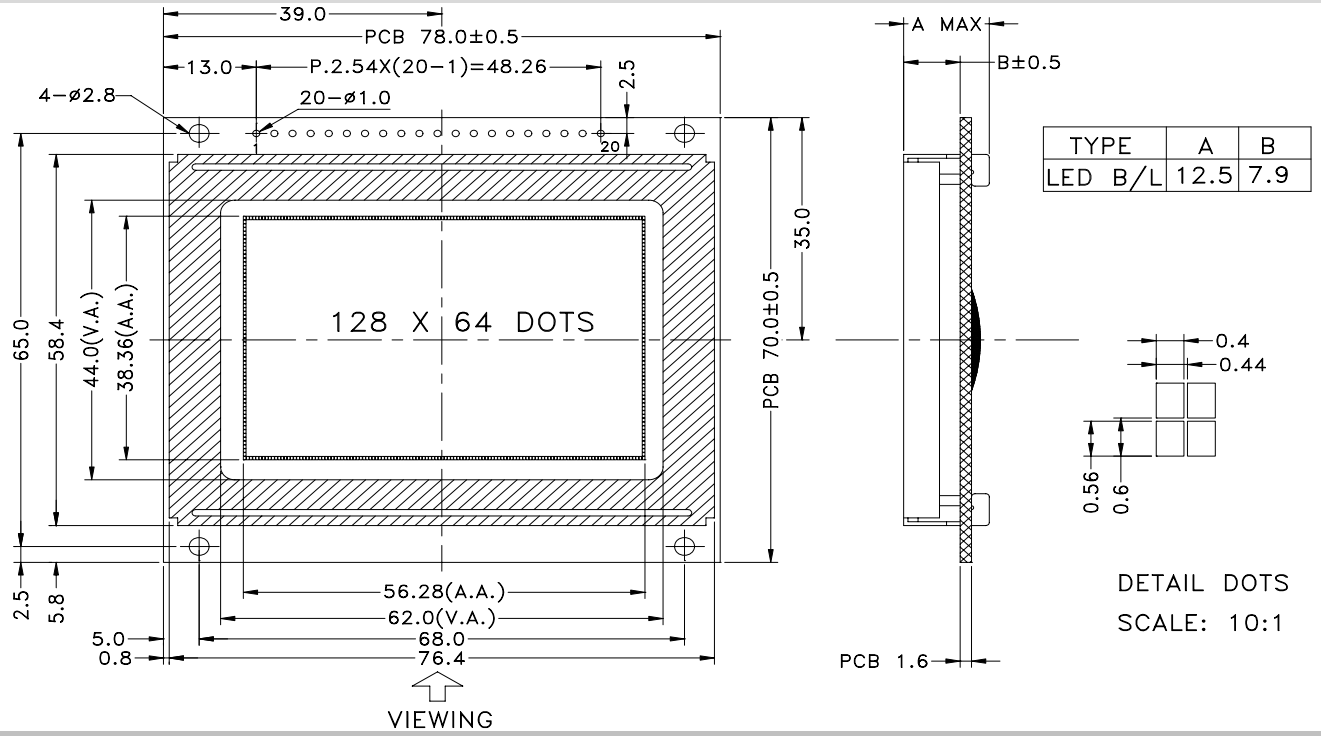
KLS9-12864H

128x64 Graphic Type LCD Module

Outline Dimension



Graphic Type

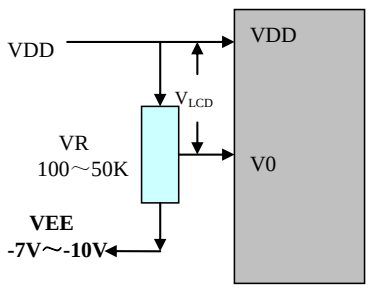
Feature：

1、128x64 dot-matrix
2、**STN/Transflective/Positive/Yellow-Green**
3、**Backlight type: Yellow Green/Side Backlight**
4、Operating Temp.: -20℃～+70℃
5、1/64 duty cycle, 1/9 Bias
6、Built-in Controller（SBN0064G or equivalent）
7、Viewing angle: 6 o'clock

Absolute Maximum Rating：

Item	Symbol	Standard value			Unit
		M_{IN}	T_{YP}	M_{AX}	
Power supply for logic	V_{DD}-V_{SS}	-0.3	--	7.0	V
Input voltage	V_I	-0.3	--	V_{DD} +0.3	V

Adjusting Display Contrast：



Note：Adjust V0 to VSS as an initial setting. When the module is operational, readjust V0 for optimal display appearance.

Electrical Characteristic： (VSS=0V, Ta = 25℃)

Parameter	Symbol	Condition	M_{IN}	T_{YP}	M_{AX}	Unit
Supply voltage for logic	V_{DD}	--	4.8	5.0	5.2	V
Supply current for logic	I_{DD}	--	--	2.5	--	mA
Operating voltage for LCD	V_{LCD}	-20℃	--	--	--	V
		+25℃	--	9.0	--	V
		+70℃	--	--	--	V
Supply voltage for Backlight	V_{BL}	--	--	5.0	--	V
Supply current for Backlight	I_{BL}	--	--	60	--	mA

Interface Pin Connections：

Pin No.(CN1)	Symbol	Level	Description
1	/CS1	H/L	Chip Selection When /CS1=0, Left area is Selection
2	/CS2	H/L	Chip Selection When /CS2=0, Right area is Selection
3	VSS	0V	Ground.
4	VDD	+5.0V	Supply voltage for logic operating.
5	V0	--	Adjusting voltage for LCD driving (variable).
6	D/I	H/L	In parallel bus mode register select 1: Data Register, 0: Instruction Register, Busy flag-Address Counter.
7	R/W	H/L	In parallel bus mode Read write control 0: write 1: read
8	E	H/L	In parallel bus mode Enable trigger
9-16	DB0-DB7	H/L	8-bit bi-directional data bus.
17	RST	H/L	System reset low active
18	VEE	H/L	Exlemal negative power supply for LCD bias
19	LED+	+5.0V	Power supply for Backlight
20	NC	--	